



PATENT

**IN THE UNITED STATES PATENT
AND TRADEMARK OFFICE**

Applicants: Dong et al.

Serial No.: 10/722,814

Filed: November 26, 2003

For: METHOD OF FORMING
GATE ELECTRODE IN
SEMICONDUCTOR DEVICE

Group Art Unit: 1765

Examiner: Duy Vu Nguyen Deo

I hereby certify that this paper (or fee) is being deposited with the United States Postal Service, first class postage prepaid, addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450

October 4, 2005

James P. Zeller
Reg. No. 28,491

AMENDMENT "A"

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Please amend this application as follows:

the amendments to the claims section begins on page 2 of this paper;

the remarks section begins on page 4 of this paper.